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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/702,234	11/06/2003	Rajesh Khamankar	TI-33223	7943	
23494 7	590 12/20/2004		EXAMINER		
TEXAS INST	RUMENTS INCORPO	HA, NATHAN W			
P O BOX 6554 DALLAS, TX		ART UNIT	PAPER NUMBER		
21122110, 111	75205		2814		

Please find below and/or attached an Office communication concerning this application or proceeding.

					<i>,</i>			
Office Action Summary		Application	n No.	Applicant(s)				
		10/702,23	4	KHAMANKAR ET AL.				
		Examiner		Art Unit				
		Nathan W.	На	2814				
The MAILIN Period for Reply	IG DATE of this communication	appears on the	cover sheet with the c	correspondence ad	ldress			
THE MAILING DA - Extensions of time may after SIX (6) MONTHS - If the period for reply sp - If NO period for reply is - Failure to reply within the Any reply received by the	TATUTORY PERIOD FOR RETE OF THIS COMMUNICATION be available under the provisions of 37 CFR from the mailing date of this communication. Described above is less than thirty (30) days, a specified above, the maximum statutory per lessed or extended period for reply will, by state office later than three months after the maximum. See 37 CFR 1.704(b).	N. R. 1.136(a). In no ever reply within the statu- iod will apply and will atute, cause the appl	ent, however, may a reply be tin utory minimum of thirty (30) day Il expire SIX (6) MONTHS from ication to become ABANDONE	nely filed /s will be considered timel the mailing date of this c				
Status								
1) Responsive	to communication(s) filed on 08	3 October 2004	4.					
2a) ☐ This action i		his action is n						
•	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims	s							
4a) Of the ab 5)	1 is/are pending in the applicate pove claim(s) 9-11 is/are withdrace is/are allowed. I is/are rejected. I is/are objected to. I are subject to restriction an	awn from cons						
Application Papers								
10) The drawing Applicant ma Replacement	ation is objected to by the Exam (s) filed on is/are: a) a y not request that any objection to drawing sheet(s) including the cor declaration is objected to by the	accepted or b) the drawing(s) b rection is require	e held in abeyance. Se ed if the drawing(s) is ob	e 37 CFR 1.85(a). ojected to. See 37 C				
Priority under 35 U.S	s.C. § 119							
12) Acknowledgr a) All b) Certifi 2. Certifi 3. Copie	ment is made of a claim for fore Some * c) None of: led copies of the priority docum led copies of the priority docum les of the certified copies of the priority form the International Bur led detailed Office action for a	ents have bee ents have bee priority docume reau (PCT Rule	n received. n received in Applicati ents have been receive e 17.2(a)).	ion No ed in this National	Stage			
	on's Patent Drawing Review (PTO-948) re Statement(s) (PTO-1449 or PTO/SB		4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal F 6) Other:	ate	O-152)			

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DETAILED ACTION

Election/Restrictions

1. Applicant's election without traverse of Group II claims 1-8 in the reply filed on 10/8/04 is acknowledged. Therefore, claims 9-11 have been withdrawn.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1-3 and 6-7 are rejected under 35 U.S.C. 102(e) as being anticipated by Lahaug, US 6,716,685.

In regard to claims 1, 6 and 7, in figs. 1a-5, Lahaug discloses a method for forming MOS transistor gate dielectrics (title), comprising:

providing a semiconductor substrate 11 (col. 3, line 47);

forming a first dielectric layer 18 on said semiconductor substrate(col. 2, line 41);

performing a first plasma nitridation of said first dielectric layer (col. 2, lines 50-

53);

removing said first dielectric from a region of said substrate (col. 3, lines 50-52);

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forming a second dielectric layer 30 on said semiconductor substrate in said region from which said first dielectric layer were removed; and

simultaneously performing a second plasma nitridation, reoxidized, of said second dielectric layer and said first dielectric layer (col. 4, lines 5-14).

In regard to claims 2 and 3, the oxide layers as mentioned above are silicon oxide. Silicon Oxide is generally mentioned in a gate of a transistor as oxide (see col. 1, lines 9-14.)

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 4-5 and 8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lahaug as applied to claims 1-3 and 6-7 above, and further in view of Chau et al. (U.S. Patent No. 6,087,236, hereinafter, Chau.)

In regard to claims 4-5 and 8, Lahaug is shown to teach all the claimed limitations as mentioned above with the exception of incorporating 5-15 and 5-20 atomic percent in the plasma nitridation into the dielectric layers.

Chau, in figs. 15-18, discloses an analogous device including a substrate 102, double gate oxides 106 and 115 formed by nitridation process (col. 5, lines 44-55) the

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teaching further includes a step of incorporating less than 3 to 30 atomic percent of nitrogen into the dielectric layer (110) by simultaneously exposing the third dielectric layer and the second dielectric layer (106) to a nitrogen containing plasma (see Fig. 8, col. 5, 33-45 and col. 6, lines 47-65). These percentages are appropriately close to the range of the percentages as claimed.

Therefore, it would have been obvious to one having ordinary skill in the art at the time of invention was made to incorporate nitrogen into the dielectric layer of Lahaug as taught by Chau to form devices of different characteristics in a substrate, CMOS, since the concentration of the nitrogen plays an importance role in the step of making the dielectric layer, for example, the layer's characteristics, which controls the threshold voltage since nitrogen prevents "holes" from penetrating into the layer.

Generally, differences in concentration or temperature will not support the patentability of subject matter encompassed by the prior art unless there is evidence indicating such concentration or temperature is critical. "[W]here the general conditions of a claim are disclosed in the prior art, it is not inventive to discover the optimum or workable ranges by routine experimentation." In re Aller, 220 F.2d 454, 456, 105 USPQ 233, 235 (CCPA 1955) (Claimed process which was performed at a temperature between 40°C and 80°C and an acid concentration between 25% and 70% was held to be prima facie obvious over a reference process which differed from the claims only in that the reference process was performed at a temperature of 100°C and an acid concentration of 10%.)

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Conclusion

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6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nathan W. Ha whose telephone number is (571) 272-1707. The examiner can normally be reached on M-TH 8:00-7:00(EST).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Nathan Ha

December 15, 2004